

**TRANSMITTAL LETTER
(General - Patent Pending)**

Docket No.
BUR920000228US1

In Re Application Of: **Malinowski et al.**

Serial No.
09/683,027

Filing Date
11/9/01

Examiner
Thai, Luan C.

Group Art Unit
2827

Title: **DUAL CHIP STACK METHOD FOR ELECTRO-STATIC DISCHARGE PROTECTION OF
INTEGRATED CIRCUITS**

TO THE COMMISSIONER FOR PATENTS:

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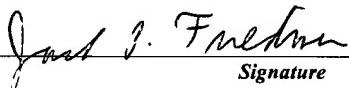
Petition Under 37 CFR 1.181(a)- 1 page

RCE- 1 page

C py of Response filed on December 9, 2002- 12 pages (including confirmation)

in the above identified application.

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Dated: **11/10/2003**

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